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APPLICANTS

Masayuki Furumiya, Kanagawa, JAPAN;
 Ryota Yamamoto, Kanagawa, JAPAN;

** CONTINUING DATA ***** *no / HT*

** FOREIGN APPLICATIONS ***** *yes HT*
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Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>HT</i>	JAPAN	14	20	6

ADDRESS
 Darryl G. Walker
 WALKER & SAKO, LLP
 Suite 235
 300 South First Street
 San Jose , CA
 95113

TITLE
 Semiconductor integrated circuit and method of manufacturing the same

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